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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
AND REVOCATION OF PRIOR POWERS
AND CHANGE OF CORRESPONDENCE ADDRESS

I, Meredith McKenzie, Manager of Intellectual Property of Cypress Semiconductor Corp., having a place of business at 3939 North First Street, San Jose, CA 95134, assignee of the entire right, title and interest of the patent applications listed on the attached Exhibit A, represent that I am empowered to sign on behalf of assignee.

As assignee of record of the entire interest of the above identified application, all powers of attorney previously given are hereby revoked and the following attorneys and/or patent agents are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith:



20575

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EXHIBIT A
POWER OF ATTORNEY AND REVOCATION OF PRIOR POWERS
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Docket No.	Inventors	Title	App. No.	App. Date	Patent No.	Issue Date	Assignment Reel/Frame	Recordation Date
5087-085	PAUL BEARD ENRIQUE ALFARO	METHOD AND APPARATUS FOR TIME AND SPACE DOMAIN SHIFTING OF BROADCAST SIGNALS	09/791,029	2/20/01			012169/0663	9/17/2001
5087-086	PAUL BEARD ENRIQUE ALFARO	CONTENT PLAYER FOR BROADCASTING TO INFORMATION APPLIANCES	09/792,388	2/23/01			012169/0615	9/17/2001
5087-087	PAUL BEARD ENRIQUE ALFARO	CONTENT CONVERTING SYSTEM AND METHOD	09/482,219	1/13/00			011199/0831	10/23/2000
5087-088	GARY A. GIBBS DONATO MONTANARI	TWO TRANSISTOR MEMORY CELL	09/678,042	10/3/00			011198/0481	10/3/2000
5087-089	ANTHONY DUNNE	OUTPUT BUFFER METHOD AND APPARATUS WITH ON RESISTANCE AND SKEW CONTROL	09/808,488	3/13/01	6,542,004	4/1/03	011608/0966	3/14/2001
5087-090	TIMOTHY M. LACEY SATISH SARIPELLA	VOLTAGE REGULATOR	09/730,315	12/5/00	6,373,231	04/16/02	011337/0075	12/5/2000
5087-091	GREG J. LANDRY ROBERT M. REINSCHMIDT TIMOTHY M. LACEY	PROGRAMMABLE SWITCH	09/740,106	12/18/00	6,486,712	11/26/02	011399/0590	12/18/2000
5087-092	PHILIP M. DANIELL GRAHAME K. REYNOLDS	HIGH VOLTAGE SWITCH WITH NO LATCH-UP HAZARDS	09/793,359	2/26/01			011576/0893	2/26/2001

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Docket No.	Inventors	Title	App. No.	App. Date	Patent No.	Issue Date	Assignment Reel/Frame	Recordation Date
5087-093	COLIN DAVIDSON	LOW STRESS TEST MODE	09/813,605	3/21/01			011638/0263	3/21/2001
5087-094	COLIN DAVIDSON	BUFFER IMPROVEMENT	09/813,572	3/21/01	6,504,413	01/07/03	011630/0914	3/21/2001
5087-095	GAJENDER ROHILLA	NEW TOPOLOGY ON VCSEL DRIVER	09/801,409	3/8/01			011588/0906	3/8/2001
5087-096	PAUL BEARD	INTERMEDIATE FREQUENCY TUNER	10/150,677	5/17/02			012925/0212	5/17/2002
5087-097	PAUL BEARD	BALL GRID ARRAY ANTENNA	10/147,827	5/17/02			012921/0235	5/17/2002
5087-099	PAUL BEARD	SHORT RANGE RADIO	10/147,839	5/17/02			012925/0255	5/17/2002
5087-100	PAUL BEARD	DIGITAL SIGNAL PROCESSOR TRANSCEIVER	10/150,731	5/17/02			012921/0273	5/17/2002
5087-101	PAUL BEARD	RANDOM NUMBER GENERATOR	10/147,828	5/17/02			012921/0265	5/17/2002
5087-102	TIMOTHY J. WILLIAMS STEVEN P. LARKY DAVID G. WRIGHT	FLEXIBLE CONVERTER	09/966,979	9/28/01			012221/0498	9/28/2001
5087-103	PAUL BEARD	DUAL PROCESSOR FRAMER	10/147,765	5/17/02			012921/0434	5/17/2002
5087-104	TIMOTHY J. WILLIAMS STEVEN P. LARKY DAVID G. WRIGHT	FLEXIBLE CONVERTER ROUTING APPARATUS, SYSTEM AND METHOD	09/967,260	9/28/01			012230/0071	9/28/2001

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Docket No.	Inventors	Title	App. No.	App. Date	Patent No.	Issue Date	Assignment Reel/Frame	Recordation Date
5087-106	KENELM G. MURRAY	ARCHITECTURE FOR PROGRAMMABLE ON-CHIP TERMINATION	10/102,256	3/19/02			012726/0508	3/19/2002
5087-107	KENELM G. MURRAY	PROGRAMMABLE ON-CHIP TERMINATION DEVICE	10/136,111	5/1/02			012863/0713	5/1/2002
5087-108	LAWRENCE RAGAN MARK GEHRING	FULLY INTEGRATED DIGITAL FREQUENCY SYNTHESIZER	09/229,063	1/12/99	6,188,288	02/13/01	011131/0709	8/29/2000
5087-109	LAWRENCE RAGAN MARK GEHRING	PRECISE INTEGRATED CURRENT MIRRORS	09/527,181	3/17/00	6,392,489	05/21/02	012728/0298	4/2/2002